Techniques for Harmonic Analysis

A. Medina, Senior Member, IEEE

Abstract-- This paper describes the gained experience on the development and application of techniques for harmonic analysis of nonlinear power systems. These methodologies have been developed in the time, frequency and hybrid time and frequency domain frames of reference. Their application to the computation of the periodic steady state solution of different test systems is detailed, indicating their advantages and limitations in terms of efficiency, computer requirements and accuracy.

Index Terms—Analysis, hybrid, nonlinear, time-varying, hybrid, periodic steady state.

I. INTRODUCTION

IMPORTANT practical experience gathered on diverse aspects of the harmonic distortion, such as its causes, standards, mitigation, as well as its effect on the quality of

power in power systems has been compiled and made available in the open literature [1-2].

Harmonic detection and harmonic prediction are currently the two main fields of the digital harmonic analysis, which allow an evaluation and diagnostics of the quality of power. The first determines and processes in real time the information of the monitored harmonic content in the network, whereas the later predicts the harmonic distortion by means of analytical models implemented for digital simulation. To this category belong the techniques described in this contribution.

In general, harmonic simulation techniques can be identified as frequency domain, time domain and hybrid time and frequency domain methods. In the sections to follow a description is given on the conceptual and analytical details on which rely the techniques previously mentioned.

II. METHODS AND ALGORITHMS

Frequency Domain. Essentially, available techniques in the frequency domain are broadly divided in current source method, iterative harmonic analysis and harmonic power flow methods.

A. Current Source Method

The frequency response of the power network, as seen by a particular bus, is obtained injecting a one per unit current or voltage at the bus of interest with discrete frequency steps for the particular range of frequencies. The process is based on the solution of the network equation,

$$[Y]V = I \tag{1}$$

where [Y] is the network admittance matrix, V is the nodal vector to be solved and I is the known vector of current injection, with only one nonzero entry.

The simplest current source method uses the sequence components frame of reference to obtain the propagation of characteristic harmonic currents by injecting ideal current sources into the power network [3]. In a later contribution, the solution of a power system is obtained directly in the phase frame of reference for three phase systems [4]. Both approaches are based on solving the entire network for each harmonic of interest, assuming harmonically decoupled circuits.

B. Iterative Harmonic Analysis (IHA)

The IHA is based on sequential substitutions of the Gausstype. The harmonic producing device is modeled as a supply voltage-dependent current source, represented by a fixed harmonic current source at each iteration. The harmonic currents are obtained by first solving the problem using an estimated supply voltage. The harmonic currents are then used to obtain the harmonic voltages. These harmonic voltages in turn allow the computation of more accurate harmonic currents. The solution process stops once the changes in harmonic currents are sufficiently small [5-9]. One of the main advantages of the IHA method is that the power network components can be modelled in a closed form, with time domain simulation or any other forms. Distorted and nondistorted conditions can be handled with this method.

However, the narrow strability margin and slow convergence characteristic of the IHA has limited its application to the solution of practical problems in power systems. Numerical dominance of the leading diagonal of the matrix of system parameters is required to ensure convergence. This is not, however, a condition satisfied by weak or poorly damped systems or near sharply tuned resonant frequencies [5-6]. A method for improving the convergence characteristics of the IHA has been proposed [7].

A. Medina is with the Facultad de Ingeniería Eléctrica, División de Estudios de Posgrado, U.M.S.N.H., Morelia, Michoacán, MEXICO (email: amedina@zeus.umich.mx)

C. Harmonic Power Flow Method (HPF)

The HPF method takes into account the voltage-dependent nature of power components. In general, the voltage and current harmonic equations are solved simultaneously using Newton-type algorithms [9-12].

The harmonics produced by nonlinear and time-varying components are cross-coupled. This phenomenon has has been already represented in detailed models of the synchronous machine [13-15], the power transformer [16], arc furnaces [17], TCRs [18] and the converter [19].

In [12] a harmonic domain solution process for the entire where nodes, phases, harmonics and network is used harmoni-coupling are explicitly represented. The solution is based on a linearization process around a particular operation point of nonlinear and time-varying components. Thus, a linear relationship between harmonic voltages and currents is possible; this is a valid condition only in a close neighborhood of the operation point. As a result of the linearization process, a Norton harmonic equivalent is obtained where the phase unbalance and harmonic cross-coupling effects are explicitly represented. The computation of the equivalent may not be easy and for obtaining accurate results it should be iteratively updated. The computational effort increases in direct proportion to the size of the analyzed system and to the number of harmonics explicitly represented. The unified iterative solution for the system has the form,

$$\Delta I = [Y_J] \Delta V \tag{2}$$

where ΔI is the vector of incremental currents having the contribution of nonlinear components, ΔV is the vector of incremental voltajes and $[Y_J]$ is the admittance matrix of linear and nonlinear components. The later components are represented for each case by the computated Norton harmonic equivalent. This is a numerically robust methodology having, in addition, good convergence characteristics [12].

In a more recent contribution [20] a Newton-Raphson method is proposed based on the instantaneous power balance formulation for the representation of linear and non-linear loads.

D. Time Domain

In principle, the periodic behaviour of an electric network can be obtained directly in the time domain by integration of the differential equations describing the dynamics of the system, once the transient response has died-out and the periodic steady state obtained [21]. This Brute Force procedure [22] may require of the integration over considerable periods of time until the transient decreases to negligible proportions. It has been suggested only for the cases where the periodic steady state can be obtained rapidly in a few cycles [6]. This is usually the case of systems where ideal sources are assumed and are, in addition, sufficiently damped. In this formulation, the general description of nonlinear and time-varying elements is achieved in terms of the following differential equation,

$$\dot{x} = f(x,t) \tag{3}$$

where x is the state vector of m elements.

The inefficient solution of (3) based on a conventional numerical integration process such as the Runge-Kutta has limited its application to obtain the periodic steady state solution of electric systems with nonlinear and time-varying components, even though in the absence of numerical instability this process leads to the "exact" solution [22].

Fast Convergence to the Limit Cycle (Steady State)

A technique has been used to obtain the periodic steady state of the systems without the the computation of the complete transient [23]. This method is based on a solution process for the system based on Newton iterations. In a later contribution [24], techniques for the acceleration of the convergence of state variables to the Limit Cycle based on Newton methods in the time domain have been introduced with the purpose of removing the severe limitations and computational inefficience of conventional Brute Force methods to obtain the periodic solutions in power systems.

Fundamentally, to derive these Newton methods it is assumed that the periodic steady state solution x(t) of (3) is *T*periodic and can be represented as a Limit Cycle for x_k in terms of other periodic element of *x* or in terms of an arbitrary *T*-periodic function, to form an orbit. Before reaching the Limit Cycle the cycles of the transient orbit are very close to it. Their position is apropiately described by their position in the Poincaré Plane [22]. A single cycle "maps" its starting point x^i to its final point x^{i+1} and also maps, from a Base Cycle [24], a segment of perturbation Δx^i to Δx^{i+1} . All the mappings close to the Limit Cycle are quasi-linear, so that a Newton method can be used to obtain the starting point x^{∞} of the Limit Cycle.

It is possible to take advantage on the linearity taking place in the neighborhood of a Base Cycle if (2) is linearized around a solution x(t) from t_i to t_{i+T} , yielding the variational problem,

$$\Delta \dot{x} = J(t) \Delta x \tag{4}$$

where J(t) is the *T*-periodic Jacobian matrix.

Note that (4) allows the application of Newton type algorithms to extrapolate the solution to the Limit Cycle, obtained as [24],

$$x^{\infty} = x^{i} + C(x^{i+1} - x^{i})$$
(5)

where

$$C = (I - \Phi)^{-1}$$
(6)

In (5) x^{∞} , x^{i} and x^{i+1} are the vectors of state variables at the Limit Cycle, beginning and end of the Base Cycle respectively, and in (6) *C*, *I* and Φ are the iteration, unit and identification matrices, respectively.

This technique has been successfully applied to the modeling in the time domain of components such as the synchronous machine [25], the power transformer [26], arc furnaces [27], TCRs [28], TSCs [29] and TCSCs [30].

E. Hybrid Methods

The fundamental advantages of the frequency and time domains are used in the hybrid methodology [24-25], where the power components are represented in their natural frames of reference, e.g., the linear in the frequency domain and the nonlinear and time-varying in the time domain. The Fig. 1 illustrates the conceptual representation of the hybrid methodology. The voltages V at the load nodes where the nonlinear components are connected are iteratively obtained. Starting from estimated V values, the currents I_L for the linear part are computated for each harmonic h using the harmonic admittance matrix $[Y_k]$, which includes non-linear load effects. For the nonlinear part, V is taken in the time domain as the periodic function v(t) to obtain i(t), which is then transformed to I_N in the frequency domain. In convergence $\Delta I = I_L + I_N$ tends to zero. The iterative solution for the entire system has the form,

 $\Delta I = [Y_k] \Delta V \tag{7}$



Fig. 1 System seen from load nodes

F. Parallel Processing.

The methods and algorithms described in the previous sections are based on different frames of reference.; each of them having associated a particular computational efficiency. There is a common characteristic between these techniques: all of them are based on a conventional sequential computer solution. In recent contributions [31-33], parallel processing technology [34-35] has been applied to further enhance the efficiency of harmonic simulation techniques. The basic idea is to solve a large problem by splitting it-up into several small tasks, which are simultaneously solved to obtain a final overall solution of the original problem. Preliminary results on harmonic analysis indicate that the application of parallel processing considerably improves the efficiency reducing the computational effort required by conventional sequential solution techniques.

III CASE STUDIES

A. Application of the Harmonic Domain

The Harmonic Domain is applied to the solution of the practical Jaguara-Taguaril transmission system [6], modified to incorporate a load to the end of the 398 km transmission line, as illustrated in Fig. 2(a). Detailed three-phase models in the Harmonic Domain of the synchronous generator, the power transformer and the transmission line have been used. The details on the analytical formulation and test data are given in [15]. The generator model incorporates the statorrotor harmonic interaction and magnetic saturation effects [15]. The transformer model takes into account a multilimb (3 o 5) magnetic core where the saturation phenomenon is represented [11]. Besides, the harmonic coupling and winding electrical connections effects are incorporated. The transmission line is represented with a frequency dependent model where long line effects are taken into account [36].

The Fig. 2(b) illustrates the response obtained at node 4. The distorted voltage waveforms and their harmonic content shown in Fig. 2(c) describe the combined effects of the intrinsic system unbalance, saturation and harmonic interaction between stator-rotor in the generator, transformer saturation, magnetic core (3 limbs), electrical configuration (grounded star-delta), frequency-dependence and long line effects of the transmission line.



Fig. 2. (a) Test system 1; (b) Voltajes at node 4; (c) Voltaje harmonic content at node 4

B. Application of Techniques for the Acceleration to the Limit Cycle.

The Fig. 3 illustrates the case of a 3 node network with magnetizing branches and arc furnaces connected at nodes 2 and 3 respectively, two shunt capacitors and three transmisión lines. The dynamic of the system is represented by eleven ordinary differential equations The source is assumed sinusoidal of 1.0 p.u. in amplitude. The Limit Cycle is located within a maximum error of 10^{-10} p.u.



Fig. 3 Test system 2.

The periodic steady state of the system is obtained in 79 periods (cycles) of time (NFC) using the Brute Force method (BF) and in 56 using the Newton methods for the acceleration of the convergence to the Limit Cycle based on the Direct Approach (DA) and Numerical Differentiation (ND) procedures, respectively [24], see Table 1. The voltage through capacitor C1 and its harmonic content are illustrated by Figs. 4(a) and (b), respectively. A considerable harmonic distortion is observed in the capacitor voltage, see Fig. 4(a), mainly produced by the strong harmonic injection coming from the arc furnaces. For this particular case large amounts of higher harmonics are produced, as observed from Fig. 4(b) where the 15th harmonic is around 30% of the fundamental

		<u> </u>	
NFC	Brute Force	DA Method	ND Method
8	2.0454e-002	2.0454e-002	2.0454e-002
20	6.6126e-004	9.4284e-003	9.4284e-003
32	2.7154e-005	4.2512e-005	4.2510e-005
44	1.1015e-006	8.6676e-010	8.6957e-010
56	4.4521e-008	8.4932e-015	1.1643e-014
:			
79	94800e-011		

Table 1. Errors during convergence of DA y ND.



Fig. 4. Voltage and harmonic content in capacitor C1.(a) Voltage v_{C1} ; (b) Harmonic content.

C. Application of the Hybrid Methodology.

The hybrid methodology has been successfully applied to obtain the periodic steady state solution of larger systems [24]. However, to date the analysis has been restricted to single phase systems, such as IEEE test systems of 14, 30, 57 and 118 nodes [37]. In Table 2 are reproduced the results obtained and reported in [24] for the 118 node test system. Three nodes are indicated where nonlinear loads of the type of the magnetizing branch of a transformer are connected. The convergence was obtained in four iterations to meet a criterium for convergence of $10^{-6} p.u$.

i adie 2. Hai	rmonic voitag	ges, IEEE-IIC	s test system.
Hanmania	Nodo 7	Node 107	No.do 110

Harmonic	Node 7	Node 107	Node 118
1	0.98913	0.99158	0.95101
3	2.637e-03	2.305e-03	1.786e-03
5	3.294e-05	1.265e-04	6.060e-05

D. Application of Parallel Processing

The Table 3 gives the relative efficiency achieved with the solution obtained for test system 2 with the sequential and the parallel computation using PVM with the ND method. For this test case seven computers were used; one taking the role of the master processor (797 MHz) and six of the slave processors (794 MHz). All computers have installed the UNIX operative system.

The relative efficiency is computed as [38],

$$E_{relative} = \frac{T_1}{T_P} \tag{7}$$

where,

 T_1 execution time with one processor

 T_P execution time with P processors

Note from Table 3 that the use of a slave processor is equivalent to the sequential solution process. The relative efficiency increases with the number of slave processors used. For the analyzed case the use of four slave processors results on a relative efficiency improvement varying between 1.0 and 1.7336 with respect to the sequential solution using 512 time steps per period, whereas for 4096 time steps per period this variation goes from 1.0 to 1.8520.

 Table 3 Sequential vs parallel solution comparison using

 PVM

Number of	Time steps per period			
Slave	512	1024	2048	4096
Processors				
1	1.0	1.0	1.0	1.0
2	1.3911	1.4444	1.4583	1.4369
3	1.6121	1.7096	1.7455	1.7250
4	1.7336	1.8227	1.8596	1.8520

Table 4 gives the relative efficiency achieved with the solution obtained with the sequential and parallel computation of the ND method using the multithreading platform. For this case a 797 MHz computer with two processors was used. This computer has installed the UNIX operative system. It can be noticed that there is a significant increase on the relative efficiency with the use of two threads, e.g. from 1.0 to 1.4824 with 512 time steps per period and from 1.0 to 1.5081 with 4096 time steps per period. However, the efficiency remains nearly constant with additional threads.

 Table 4 Sequential vs parallel solution comparison using threads

Number	Time steps per period			
of	512	1024	2048	4096
threads				
1	1.0	1.0	1.0	1.0
2	1.4444	1.4461	1.4449	1.4430
3	1.4824	1.4878	1.4933	1.5005
4	1.4824	1.4911	1.5000	1.5073
:			:	:
11	1.4824	1.4977	1.5033	1.5081

III. CONCLUSIONS

A description has been given on the fundamentals of the techniques for the harmonic analysis in power systems, developed in the frames of reference of frequency, time and hybrid time-frequency domain, respectively. The details on their formulation, potential and iterative process has been given.

In general Harmonic Power Flow methods are numerically robust and have good convergence properties. Howerver, their application to obtain the non-sinusoidal periodic solution of the power system may require the iterative process of a matrix equation problem of very high dimensions.

Conventional Brute Force methodologies in the time domain for the computation of the periodic steady state in the power system are in general an inefficient alternative which, in addition, may not be sufficiently reliable, in particular for the solution of poorly damped systems. The potential of the Newton techniques for the acceleration to the Limit Cycle has been illustrated. Their application yields efficient time domain periodic steady state solutions.

The principles of the hybrid methodology of solution have been given and its potential has been indicated for the solution of larger single phase systems. It is an interesting alternative of solution in merit to its ability to represent the system components in their natural frame of reference, leading to efficient, robust periodic steady state time solutions for the complete network. To date it has been successfully applied to the solution of single phase systems, being in progress its application to the periodic steady state solution of practical three phase systems.

Preliminary results on the application of parallel processing in harmonic analysis indicate that this technology can substantially enhance the original computer efficiency of existing harmonic simulation techniques. This is a field in need of further investigation.

IV. ACKNOWLEDGMENT

The author gratefully acknowledges the Universidad Michoacana de San Nicolás de Hidalgo through the División de Estudios de Posgrado of the Facultad de Ingeniería Eléctrica for the facilities granted to carry-out this investigation.

V. REFERENCES

[1] J. Arrillaga, D.A. Bradley, P.S. Bodger, *Power System Harmonics*, John Wiley and Sons, 1985.

[2] M.H.J. Bollen, Understanding Power Quality Problems, IEEE Press, 2000.

[3] A.A. Mahmoud, R.D. Schultz, "A Method for Analyzing Harmonic Distribution in A.C. power Systems", *IEEE Trans. on Power Apparatus and Systems*, Vol. PAS-101, No. 6, PP. 1815-1824, 1982.

[4] T.J. Demsem, P.S. Bodger, J. Arrillaga, "Three Phase Transmission System Modelling for Harmonic Penetration Studies", *IEEE Trans. on Power Apparatus and Systems*, Vol. PAS-103, No. 2, pp. 310-317, Feb. 1984.

[5] C.D. Callaghan, J. Arrillaga, "Convergence Criteria for Iterative Harmonic Analysis and its Application to Static Convertors", in *Proc. 1990 IEEE/ICHPS IV International Conference on Harmonics in Power Systems*, Budapest, Hungary, October 4-6, pp. 38-43.

[6] H.W. Dommel, A Yan, S. Wei, "Harmonics from Transformer Saturation", *IEEE Trans. on Power Systems*, Vol. PWRD-1, No. 2, pp. 209-214, Apr. 1986.

[7] C.D. Callaghan, J. Arrillaga, "A Double Iterative Algorithm for Iterative Harmonic Analysis and Harmonic Flows at ac-dc Terminals", *Proc. of the IEE*, Vol. 136, No. 6, 1989, pp. 319-324.

[8] V. Sharma, R.J. Fleming, L. Niekamp,"An Iterative Approach for Analysis of Harmonic Penetration in Power transmission Networks", *IEEE Trans. on Power Delivery*, Vol. 6, No. 4, pp. 1698-1706, Oct. 1991.

[9] B.C. Smith, J. Arrillaga, A.R. Wood, N.R. Watson, "A Review ofIterative Harmonic Analysis for AC-DC Power Systems", *IEEE Trans. on Power Delivery*, Vol. 13, No. 1, pp. 180-185, Jan. 1998.

[10] D. Xia, G.T. Heydt, "Harmonic Power Flow Studies, Part I – Formulation and Solution, Part II – Implementation and Practical Application", *IEEE Trans. on Power Apparatus and Systems*, vol. PAS-101, pp. 1257-1270, June 1982.

[11] W. Xu, J.R. Marti, H.W. Dommel "A multiphase harmonic load flow solution technique", *IEEE Trans. on Power Systems*, vol. PS-6, pp. 174-182, Feb. 1991.

[12] J. Arrillaga, A. Medina, M.L.V. Lisboa, M.A. Cavia, P. Sánchez, "The Harmonic Domain a Frame of Reference for Power System Harmonic Analysis", *IEEE Trans. on Power Systems*, Vol. 10, No. 1, pp. 433-440, Feb. 1995.

[13] W.W. Xu, J.R. Marti, H.W. Dommel, "A Synchronous Machine Model for Three-Phase Harmonic Analysis and EMTP Initialization", *IEEE Trans. on Power Systems*, Vol. 6, No. 4, pp. 1530-1538, Nov. 1991.

[14] A. Semlyen, J.F. Eggleston, J. Arrillaga, "Admittance Matrix Model of a Synchronous Machine for Harmonic Analysis", *IEEE Trans. on Power Systems*, Vol. PWRS-2, No. 4, pp. 833-840, Nov. 1987.

[15] A. Medina, J. Arrillaga, J.F. Eggleston, "A Synchronous Machine Model in the Harmonic Domain", *in Proc. IEEE ICEM92 International Conference on Electrical Machines*, Manchester, UK, pp. 647-651.

[16] A. Medina, J. Arrillaga, "Generalised Modelling of Power Transformers in the Harmonic Domain", *IEEE Trans. on Power Delivery*, Vol. 7, No. 3, July 1992, pp. 1458-1465, Sept. 1992.

[17] E. Acha, A. Semlyen, N. Rajakovic, "A Harmonic Domain Computational Package for Nonlinear Problems and its Application to Electric Arcs", *IEEE Trans. on Power Delivery*, Vol. 5, No. 3, pp. 1390-1397, July 1990.

[18] E. Acha, J.J. Rico, S. Acha, M. Madrigal, "Harmonic Domain Modelling of the Three Phase Thyristor-Controlled Reactors by Means of Switching Vectors and Discrete Convolutions", *IEEE Trans. on Power Delivery*, Vol. 11, No. 3, pp. 1678-1684, 1996.

[19] B.C. Smith, A. Wood, J. Arrillaga, "A Steady State Model of the AC-DC Converter in the Harmonic Domain", *IEE Proc. Generation, Transmission and Distribution*, Vol. 142, No. 2, pp. 109-118, 1995.

[20] M. Madrigal, E. Acha, "A New Harmonic power Flow Method Based on the Instantaneous power Balance", *in Proc. of the 10th IEEE/ICHQP International Conference on Harmonics and Quality of Power*, Rio de Janeiro, Brazil, October 6-9, 2002.

[21] H. W. Dommel, "Digital Computer Solution of Electromagnetic Transients in Single and Multiphase Networks", *IEEE Trans. on Power Apparatus and Systems*, Vol. PAS-88, No. 4, pp. 388-399, April 1969.

[22] T.S. Parker, L.O. Chua, *Practical Numerical Algorithms for Chaotic Systems*, Springer-Verlag, 1989.

[23] T.J. Aprille, T.N. Trick, "A Computer Algorithm to Determine the Steady State Response of Nonlinear Oscillators", *IEEE Trans. on Circuit Theory*, Vo. 9, No. 4, pp. 354-360, 1972.

[24] A. Semlyen, A. Medina, "Computation of the Periodic Steady State in Systems with Nonlinear Components Using a Hybrid Time and Frequency Domain Methodology", *IEEE Trans. on Power Systems*, Vol. 10, No. 3, pp. 1498-1504, Aug. 1995.

[25] O. Rodríguez, A. Medina, "Fast Periodic Steady State Solution of a Synchronous Machine Model in Phase Coordinates Incorporating the Effects of Magnetic Saturation and Hysteresis", *in Proc. 2001 IEEE PES Winter Meeting*, Vol. 3, January 28 – February 1, 2001, Columbus, Ohio, USA, pp. 1431-1436.

[26] S. García, A. Medina, "A State Space Three-Phase Multilimb Transformer Model in the Time Domain: Fast Periodic Steady State Analysis", *in Proc. 2001 IEEE PES Summer Meeting*, Vol. 3, July 2001, Vancouver, Canada, pp. 1859-1864.

[27] A. Medina, N. García, "Dynamic Análisis of Electric Arcs Using a Time Domain Newton Technique", *in Proc. 1998 IEEE International Power Electronics Congress*, October 1998, Morelia, México, pp. 82-88.

[28] N. García, A. Medina, "Efficient Computation of the Periodic Steady-State Solution of Systems Containing Nonlinear and Time-Varying Components. Application to the Modeling of TCRs", *in Proc. of the 9th IEEE/ICHQP Internacional Conference on Harmonics and Quality of Power*, Orlando FL, USA; 2000, Vol. 2, pp. 673-678.

[29] N. García, A. Medina, "Fast Periodic Steady State Solution of Systems Containing Thyristor Switched Capacitors", *in Proc. 2000 IEEE PES Summer Meeting*, Seattle, USA; July 2000, Vol. 2, pp. 1127-1132.

[30] A. Medina, A. Ramos-Paz, C.R. Fuerte-Esquivel, "Fast Periodic Steady State of Systems Containing TCSCs", *in Proc. of the 10th IEEE/ICHQP International Conference on Harmonics and Quality of Power*, Rio de Janeiro, Brazil, October 6-9, 2002.

[31] N. Garcia, E. Acha, and A. Medina, "Swift Time Domain Solutions of Electric Systems Using Parallel Processing", *in Proc. of the Sixth IASTED International Conference*, Rhodes, Greece, July 2001, pp. 172-177.

[32] A. Medina, A. Ramos-Paz, C.R. Fuerte Esquivel, "Efficient Computation of the Periodic Steady State Solution of Systems with Nonlinear Components Applying Parallel Multi-Processing", *in Proc. 2002 IEEE PES Summer* Meeting, Chicago II, USA, Vol. 3, pp. 1483-1487.

[33] A. Medina, A. Ramos-Paz, C.R. Fuerte Esquivel, "Periodic Steady State Solution of Electric Systems with Nonlinear Components Using Parallel Processing", *IEEE PES Power Engineering Review*, to be published.

[34] Geist, A. Beguelin, A. and Dongarra, J., *PVM: Parallel Virtual Machine, MIT Press 1994.*

[35] Sun, Multithreaded Programming Guide, Aug. 1997.

[36] A. Medina, "Power System Modelling in the Harmonic Domain", *PhD Thesis*, University of Canterbury, New Zealand, 1992.

[37] L.L. Freris, A.M. Sasson, "Investigation of the Load Flow Problem", *Proceedings of the IEE*, Vol. 115, No. 10, October 1968, pp. 1450-1460.

[38] I. Foster ,.: *Designing and Building Parallel Programs*, Addison Wesley, 1994.

VI. BIOGRAPHY

Aurelio Medina (SM'02) obtained his Ph.D. from the University of Canterbury, Christchurch, New Zealand in 1992. He has worked as a Post-Doctoral Fellow at the Universities of Canterbury, New Zealand (1 year) and Toronto, Canada (2 years). At present he is a staff member of the Facultad de Ingeniería Eléctrica, UMSNH, Morelia, Mexico where he is the Head of the Division for Posgraduate Studies. He is Senior Member of IEEE and is listed in the book Who's Who in the World. His research interests are in the dynamic and steady state analysis of power systems.